



Attorney Docket No. SEL 154

#4 155
10-16-01
R. Stiles

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of

Shunpei YAMAZAKI

Serial No.: 09/479,262

Filed: January 5, 2000

Art Unit: 2815

For: Semiconductor Device And Method
Of Manufacturing The Same

I hereby certify that this correspondence
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Commissioner for Patents, Washington,
D.C. 20231, on September 22, 2001

Shunpei Yamazaki

Date: 9-27-01

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Pursuant to 37 C.F.R. §1.97, as revised on February 4, 1992, 1135 OG 23-24, Applicant hereby call the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. A copy of each of the documents is enclosed herewith for the Examiner's consideration.

Other applications related to the present invention include:

US Patent Application No. 09/472,079 (pending)

No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

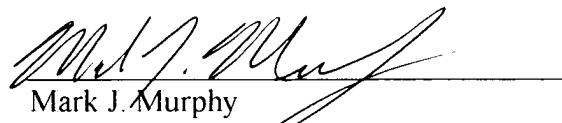
The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not to be construed as an admission that any subject matter disclosed in the document is necessarily

within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

Please charge our Deposit Account No. 50/1039 for the amount for this IDS.

Respectfully submitted,



Mark J. Murphy
Registration No. 24,225

COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, Ltd.
200 West Adams Street, Suite 2850
Chicago, Illinois 60606
(312) 236-8500



LIST OF PUBLICATIONS CITED BY APPLICANT	<u>Attorney Docket No.</u> SEL 154	<u>Serial No.</u> 09/479,262
	<u>Applicant</u> Shunpei YAMAZAKI	
	<u>Filing Date</u> January 5, 2000	<u>Group</u>

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	4,990,365	02/05/91	Treichel et al.			06/16/89
	5,232,862	08/03/93	Heremans et al.			01/13/92
	5,247,190	09/21/93	Friend et al.			04/18/90
	5,324,690	06/28/94	Gelatos et al.			02/01/93
	5,373,171	12/13/94	Imai et al.			03/09/88
	5,399,502	03/21/95	Friend et al.			05/05/93

FOREIGN PATENT DOCUMENTS

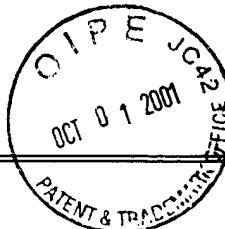
	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	EP 0 394 054	10/24/90	Gomi			04/20/90
	EP 0 566 838	10/27/93	Kawamura et al.			02/18/93
	EP 0 999 595	05/10/00	Yamazaki et al.			11/02/99
	JP 62-156822	07/11/87	Takahiro			12/27/85
	JP 63-111173	05/16/88	Mikio			10/30/86

OTHER PUBLICATIONS (Including Author, Title, Date, Pertinent Pages)

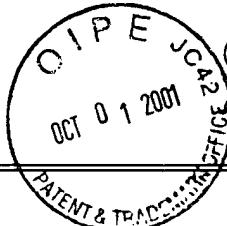
1) JP 62-156822 English abstract.
2) JP 63-111173 English abstract.

EXAMINER:	DATE CONSIDERED:
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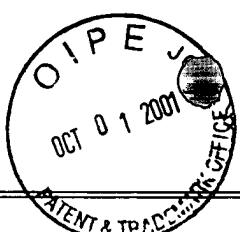
*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.



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U.S. PATENT DOCUMENTS						
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	5,536,360	07/16/96	Nguyen et al.			01/03/95
	5,643,826	07/01/97	Ohtani et al.			10/25/94
	5,756,250	05/26/98	Hirai et al.			06/07/95
	5,923,962	07/13/99	Ohtani et al.			04/28/95
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	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	JP 63-120429	05/24/88	Katsuya			11/10/86
	JP 02-179947	07/12/90	Yujiro			01/04/89
	JP 02-234438	09/17/90	Naohiro			03/08/89
	JP 03-097846	04/23/91	Katsunori			09/07/89
	JP 04-367277	12/18/92	Osamu			06/14/91
OTHER PUBLICATIONS (Including Author, Title, Date, Pertinent Pages)						
	3) JP 63-120429 English abstract. 4) JP 02-179947 English abstract. 5) JP 02-234438 English abstract. 6) JP 03-097846 English abstract. 7) JP 04-367277 English abstract.					
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U.S. PATENT DOCUMENTS						
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	6,194,254	02/27/01	Takemura			03/28/97
FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	JP 05-152328	06/18/93	Hiroshi			11/28/91
	JP 07-130652	05/19/95	Otani et al.			10/29/93
	JP 07-278790	10/24/95	Satoru			02/21/95
	JP 08-078329	03/22/96	Otani			09/05/94
	JP 08-254713	10/01/96	Fukuda			03/16/95
	JP 08-330602	12/13/96	Tanaka et al.			03/27/96
	JP 10-092576	04/10/98	Friend et al.			04/18/97
OTHER PUBLICATIONS (Including Author, Title, Date, Pertinent Pages)						
	8) JP 05-152328 English abstract. 9) JP 07-130652 English abstract. 10) JP 07-278790 English abstract. 11) JP 08-078329 English abstract. 12) JP 08-254713 English abstract. 13) JP 08-330602 English abstract. 14) JP 10-092576 English abstract.					
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FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	JP 10-135468	05/22/98	Yamazaki et al.			10/24/96
	JP 10-135469	05/22/98	Yamazaki et al.			10/24/96
	JP 10-270363	10/09/98	Onuma et al.			03/27/97
	WO 90/13148	11/01/90	Friend et al.			04/18/90
OTHER PUBLICATIONS (Including Author, Title, Date, Pertinent Pages)						
	15) JP 10-135468 English abstract. 16) JP 10-135469 English abstract. 17) JP 10-270363 English abstract. 18) European Search Report re application no. EP 99 12 5839, dated 04/06/00. 19) European Search Report re application no. EP 99 12 5839, dated 12/07/00. 20) Maeda et al., "A Low-Permittivity Interconnection Using An SiBN Interlayer," <u>IEEE Transactions On Electron Devices</u> , Vol. 36, No. 9, pp. 1610-1614, September 1989. 21) Schenk et al., "Polymers For Light Emitting Diodes," <u>EuroDisplay '99</u> , Berlin, Germany, September 6-9, 1999, pp. 33-37. 22) US Patent Application No. 09/472,079 (pending) to Yamazaki et al., including specification, claims, abstract, drawings and PTO filing receipt.					
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